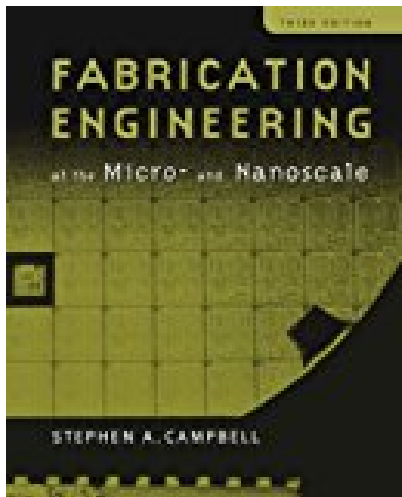


Fabrication Engineering at the Micro and Nanoscale The Oxford Series in Electrical and Computer Engineering



BOOK DETAILS

- Author : Stephen A. Campbell
- Pages : 672 Pages
- Publisher : Oxford University Press
- Language : English
- ISBN : 0195320174

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BOOK SYNOPSIS

Designed for advanced undergraduate or first-year graduate courses in semiconductor or microelectronic fabrication, *Fabrication Engineering at the Micro and Nanoscale*, Fourth Edition, covers the entire basic unit processes used to fabricate integrated circuits and other devices. With many worked examples and detailed illustrations, this engaging introduction provides the tools needed to understand the frontiers of fabrication processes. **NEW TO THIS EDITION** Coverage of many new topics including: - the flash and spike annealing processes - extreme ultraviolet (EUV) lithography - GaN epitaxial growth and doping - double exposure routes to sub-35-nm lithography - architectures for nanoscale CMOS as practiced at the 45-nm node - tri-gate or FINFET CMOS planned for 22 nm and below - bulk silicon and thin film solar cell manufacturing - GaN LED fabrication - microfluidics Updated sections on nonoptical lithography Expanded content on state-of-the-art CMOS A Companion Website with PowerPoint slides of figures from the text (www.oup.com/us/campbell) An Instructors Solutions Manual, available to registered adopters of the text (978-0-19-986121-7)

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